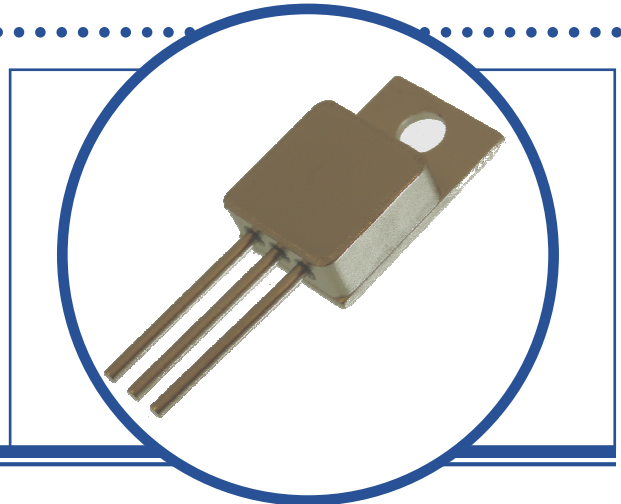


N-CHANNEL POWER MOSFET

IRFY240 / IRFY240M

- Low $R_{DS(on)}$ MOSFET Transistor
In A Hermetic Metal TO-257AB Package
- Designed For Switching, Power Supply,
Motor Control and Amplifier Applications
- Screening Options Available



ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ unless otherwise stated)

VDS	Drain – Source Voltage		200V
VGS	Gate – Source Voltage		$\pm 20\text{V}$
I_D	Continuous Drain Current	$T_C = 25^\circ\text{C}$	12A
I_D	Continuous Drain Current	$T_C = 100^\circ\text{C}$	7.8A
I_{DM}	Pulsed Drain Current ⁽¹⁾		48A
P_D	Total Power Dissipation at	$T_C = 25^\circ\text{C}$	60W
	Derate Above 25°C		0.48W/ $^\circ\text{C}$
E_{AS}	Single Pulse Avalanche Energy ⁽²⁾⁽⁵⁾		330mJ
I_{AR}	Avalanche Current ⁽¹⁾⁽⁵⁾		12A
E_{AR}	Repetative Pulse Avalanche Energy ⁽¹⁾⁽⁵⁾		6mJ
dv/dt	Peak Diode Recovery ⁽³⁾⁽⁵⁾		5V/ns
T_J	Junction Temperature Range		-55 to $+150^\circ\text{C}$
T_{stg}	Storage Temperature Range		-55 to $+150^\circ\text{C}$

THERMAL PROPERTIES

Symbols	Parameters	Max.	Units
$R_{\theta JC}$	Thermal Resistance, Junction To Case	2.1	$^\circ\text{C/W}$

INTERNAL PACKAGE INDUCTANCE

Symbols	Parameters	Min.	Typ.	Max.	Units
L_D	Internal Drain Inductance		8.7		nH
L_S	Internal Source Inductance		8.7		

Notes

- (1) Repetitive Rating: Pulse width limited by maximum junction temperature
- (2) @ $V_{DD} = 50\text{V}$, Starting $T_J = 25^\circ\text{C}$, $L = 4.5\text{mH}$, Peak $I_L = 12\text{A}$, $V_{GS} = 10\text{V}$
- (3) @ $I_{SD} \leq 12\text{A}$, $di/dt \leq 150\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, $T_J \leq 150^\circ\text{C}$, Suggested $R_G = 9.1\Omega$
- (4) Pulse Width $\leq 300\mu\text{s}$, $\delta \leq 2\%$
- (5) By Design Only, Not A Production Test.

Semelab Limited reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by Semelab is believed to be both accurate and reliable at the time of going to press. However Semelab assumes no responsibility for any errors or omissions discovered in its use. Semelab encourages customers to verify that datasheets are current before placing orders.



N-CHANNEL POWER MOSFET IRFY240 / IRFY240M

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise stated)

Symbols	Parameters	Test Conditions	Min.	Typ	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 I _D = 1.0mA	200			V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Temperature Coefficient of Breakdown Voltage	Reference to 25°C I _D = 1.0mA		0.29		V/°C
R _{DS(on)}	Static Drain-Source On-State Resistance	V _{GS} = 10V I _D = 7.8A ⁽⁴⁾			0.19	Ω
		V _{GS} = 10V I _D = 12A ⁽⁴⁾			0.22	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} I _D = 250μA	2		4	V
g _{fs}	Forward Transconductance	V _{DS} ≥ 15V I _{DS} = 7.8A ⁽⁴⁾	6.1			S(Ω)
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} = 0 V _{DS} = 0.8BV _{DSS} T _J = 125°C			25	μA
					250	
I _{GSS}	Forward Gate-Source Leakage	V _{GS} = 20V			100	nA
I _{GSS}	Reverse Gate-Source Leakage	V _{GS} = -20V			-100	

DYNAMIC CHARACTERISTICS

C _{iss}	Input Capacitance	V _{GS} = 0		1300		pF
C _{oss}	Output Capacitance	V _{DS} = 25V		400		
C _{rss}	Reverse Transfer Capacitance	f = 1.0MHz		130		
Q _g ⁽⁵⁾	Total Gate Charge	V _{GS} = 10V			60	nC
Q _{gs} ⁽⁵⁾	Gate-Source Charge	I _D = 12A			10.6	
Q _{gd} ⁽⁵⁾	Gate-Drain Charge	V _{DS} = 0.5BV _{DSS}			37.6	
t _{d(on)}	Turn-On Delay Time	V _{DD} = 100V			20	ns
t _r	Rise Time	I _D = 12A			152	
t _{d(off)}	Turn-Off Delay Time				58	
t _f	Fall Time	R _G = 9.1Ω			67	

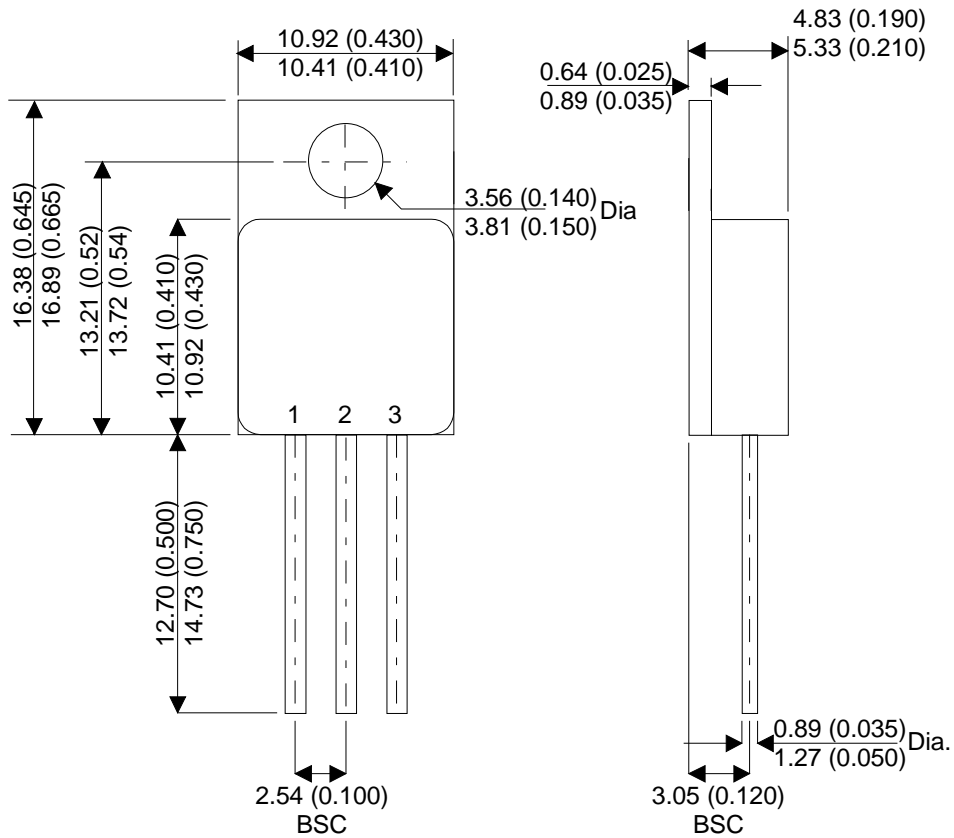
SOURCE-DRAIN DIODE CHARACTERISTICS

I _S	Continuous Source Current				12	A
I _{SM}	Pulse Source Current ⁽¹⁾				48	
V _{SD}	Diode Forward Voltage	I _S = 12A V _{GS} = 0 ⁽⁴⁾	T _J = 25°C		1.5	V
t _{rr}	Reverse Recovery Time	I _S = 12A	T _J = 25°C		500	ns
Q _{rr}	Reverse Recovery Charge	V _{DD} ≤ 50V	di/dt = 100A/μs ⁽⁴⁾		5.3	μC

N-CHANNEL POWER MOSFET IRFY240 / IRFY240M

MECHANICAL DATA

Dimensions in mm (inches)



TO220M (TO-257AB)

Part No.	Pin 1	Pin 2	Pin 3
IRFY240	Gate	Drain	Source
IRFY240M	Drain	Source	Gate